

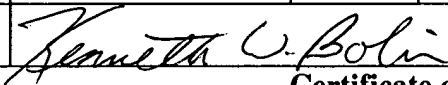


06-18-04

IFW

First Named Inventor	Leonard Forbes	TRANSMITTAL FORM UNDER 37 CFR 1.10 (LARGE ENTITY)
Serial No.	10/682,590	
Filing Date	October 9, 2003	
Group Art Unit	2818	
Examiner Name	Mai-Huong Tran	
Confirmation Number	1986	
Attorney Docket No.	400.257US01	
Title: FULLY DEPLETED SILICON-ON-INSULATOR CMOS LOGIC		

Mail Stop: AMENDMENT
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Enclosures					
The following documents are enclosed:					
<input checked="" type="checkbox"/>	Response to Restriction Requirement (1 pg.);				
<input checked="" type="checkbox"/>	a return postcard.				
Please charge any additional fees or credit any overpayments to Deposit Account No. 501373.					
<u>CUSTOMER NUMBER 27073</u>					
Leffert Jay & Polglaze, P.A. P. O. Box 581009 Minneapolis, MN 55458-1009					
Submitted By					
Name	Kenneth W. Bolvin	Reg. No.	34,125	Telephone	(612) 312-2211
Signature				Date	June 17, 2004
Certificate of Mailing					
"Express Mail" mailing label number: <u>EL 962669660 US</u> Date of Deposit: <u>June 17, 2004</u> These papers and fees are being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR §1.10 on the date indicated above and addressed to Mail Stop: AMENDMENT, Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450.					
(LARGE ENTITY TRANSMITTAL UNDER 37 C.F.R. 1.10)					



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In response to the Restriction Requirement mailed May 27, 2004, Applicant elects claims Group II, Claims 1 –16 without traverse.

Applicant respectfully cancels remaining claims 17 - 22 without prejudice, and reserves the right to reintroduce them in a divisional application at a later date.

The Examiner is invited to contact Applicant's Representative at (612) 312-2211 if there are any questions regarding this Response or if prosecution of this application may be assisted thereby.

Respectfully submitted,

Date: 6/17/04

Kenneth W. Bolvin
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Reg. No. 34,125

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